

Docket No.: 49657-551

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kazuhiro AIHARA, et al.

Serial No.: 09/499,037

Filed: February 7, 2000

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE

Group Art Unit: 2826

Examiner: J. Mondt

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

BOX AF
THE COMMISSIONER FOR PATENTS AND TRADEMARKS
Washington, DC 20231

Dear Sir:

Transmitted herewith is an Amendment in the above identified application.

- ☒ No additional fee is required.
☐ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
☐ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
☐ Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	6	20	0	\$18.00 =	\$0.00
Independent Claims	3	3	0	\$84.00 =	\$0.00
Multiple claims newly presented					\$0.00
Fee for extension of time					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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#26,106

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AMENDMENT UNDER 37 C.F.R. § 1.116

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The Commissioner for Patents and Trademarks
Washington, DC 20231

8# / Amndt B (10)

Sir:

3/22/02
DBrown

The following Amendment and Remarks are submitted in response to the Final Office Action dated December 18, 2001, pursuant to the provisions of 37 C.F.R. § 1.116. Please amend the above-identified application as follows:

Please
Enter
NJr

IN THE CLAIMS:

Please amend claim 2 as follows:

2. (Twice Amended) A semiconductor device, comprising:
- a storage electrode including a first tantalum nitride film formed over a semiconductor substrate;
 - a capacitor dielectric film including a tantalum oxide film formed on and contacting an upper surface of said first tantalum nitride film; and

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